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DATE:

August 10, 2004

FILE NO:

INFN/0076

TO:

Office of Initial Patent Examination's Filing Receipt Corrections

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703-746-9195

COMPANY:

USPTO

FROM:

Gero McClellan

PAGE(S) with cover:

4

ORIGINAL TO

FOLLOW?

☐ YES ⊠ NO

REQUEST FOR CORRECTION OF ERROR ON FILING RECEIPT

TITLE:

DATA MEMORY CIRCUIT

U.S. SERIAL NO.:

10/817,504

FILING DATE:

April 2, 2004

INVENTOR:

Knupfer, et al.

EXAMINER:

Unknown

GROUP ART UNIT:

2818

CONFIRMATION NO.:

3148

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W&B Docket No.: INF 2292-US QC Docket No.: INFN/0076

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Knüpfer, et al.

Serial No.: 10/817,504

Filed:

April 2, 2004

Confirmation No.: 3148

For:

DATA MEMORY CIRCUIT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Group Art Unit: 2818

Examiner:

Unknown

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REQUEST FOR CORRECTION OF ERROR ON FILING RECEIPT

The Filing Receipt for the above-referenced application contains an error regarding the second inventor's first name. The Filing Receipt recites the inventor's first name as "Jelmut". It should read "Helmut".

Attached is a copy of the Filing Receipt with the above change noted thereon. Please correct this error or notify the applicants of the reasons for denying the request.

Respectfully submitted,

Gero G. McClellan

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APPL NO.	FILING OR 371	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS	
10/817 504	(c) DATE	2818	874	INFN/0076	2	-21	4	

CONFIRMATION NO. 3148

GERO G. MCCLELLAN MOSER, PATTERSON & SHERIDAN, L.L.P. Suite 1500 3040 Post Oak Blvd. Houston, TX 77056

FILING RECEIPT *OC000000013043905*

Date Mailed: 06/23/2004

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Applicant(s)

Bernhard Knupfer, Otterfing, GERMANY; Jelmut Fischer, Oberhaching, GERMANY; 'Helmut

Domestic Priority data as claimed by applicant

Foreign Applications

GERMANY 103 15 528.7-55 04/04/2003

If Required, Foreign Filing License Granted: 06/23/2004

OKS, AVAILABLE CORL Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Data memory circuit

Preliminary Class

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